

## Firmware Release Notes - FL SWITCH 3000 – 4000 - 4800 Series

### 1. Firmware Version: 1.xx / 2.xx

**NOTE:** Each firmware change will require more firmware images due to the introduction of new higher capacity hardware platforms (memory size, responsiveness).

Switch Type	Firmware File
• General FL SWITCH 3000 and 40xx	FL_SWITCH_3000_4000_v1_xx.bin
• FL SWITCH 4800E Rack Mount	FL_SWITCH_4800_v1_xx.bin
• POE - FL SWITCH 4004T-xxxxxx	FL_SWITCH_4004T_v2_xx.bin
• POE - FL SWITCH 4000T-xxxxxx	FL_SWITCH_4000T_v2_xx.bin

MIB archive	
FL SWITCH 3000 - 4000	FL_SWITCH_3000_4000_MIBs_v1_xx.zip
FL SWITCH 4800	FL_SWITCH_4800_MIBs_v1_xx.zip
FL SWITCH 4000T-xPOE-xSFP	FL_SWITCH_4000T_MIBs_v2_xx.zip
FL SWITCH 4004T-xPOE-xSFP	FL_SWITCH_4004T_MIBs_v2_xx.zip

## 2. Supported Hardware

The new firmware is available for all products below.

FL SWITCH 3000 – 4000 Series uses FL\_SWITCH\_3000\_4000\_v1\_xx.bin

Order name	Order no
FL SWITCH 3005	2891030
FL SWITCH 3008	2891031
FL SWITCH 3005T	2891032
FL SWITCH 3004T-FX	2891033
FL SWITCH 3004T-FX ST	2891034
FL SWITCH 3008T	2891035
FL SWITCH 3006T-2FX	2891036
FL SWITCH 3006T-2FX ST	2891037
FL SWITCH 3012E-2FX	2891120
FL SWITCH 3012E-2FX SM	2891119
FL SWITCH 3012E-2SFX	2891067
FL SWITCH 3016	2891058
FL SWITCH 3016T	2891059
FL SWITCH 3016E	2891066
FL SWITCH 3006T-2FX SM	2891060
FL SWITCH 4008T-2SFP	2891062
FL SWITCH 4008T-2GT-4FX SM	2891061
FL SWITCH 4008T-2GT-3FX SM	2891160
FL SWITCH 4012T-2GT-2FX	2891063
FL SWITCH 4012T-2GT-2FX ST	2891161

FL SWITCH 4800 Series uses FL\_SWITCH\_4800\_v1\_xx.bin

Order name	Order no
FL SWITCH 4808E-16FX-4GC	2891079
FL SWITCH 4808E-16FX LC-4GC	2891073
FL SWITCH 4808E-16FX SM-4GC	2891080
FL SWITCH 4808E-16FX SM LC-4GC	2891074
FL SWITCH 4808E-16FX ST-4GC	2891085
FL SWITCH 4808E-16FX SM ST-4GC	2891086
FL SWITCH 4824E-4GC	2891072
FL SWITCH 4800E-24FX-4GC	2891102
FL SWITCH 4800E-24FX SM-4GC	2891104

FL SWITCH 4000T PoE Series uses FL\_SWITCH\_4000T\_v2\_xx.bin

Order name	Order no
FL SWITCH 4000T-4POE-SFP	1026924
FL SWITCH 4000T-8POE-2SFP	1026923

FL SWITCH 4004T PoE Series uses FL\_SWITCH\_4004T\_v2\_xx.bin

Order name	Order no
FL SWITCH 4004T-8POE-4SFP	1026922

### 3. History

Firmware-Version	Functions
<ul style="list-style-type: none"> <li>• 1.35</li> <li>•</li> <li>• (Dec / 10 / 2018)</li> </ul>	<ul style="list-style-type: none"> <li>• Added Functions</li> </ul> <hr/> <ul style="list-style-type: none"> <li>• Added security: Allow MAC-Based Security across multiple switch interface ports for same MAC address.</li> <li>• Added security: HTTPS key generation feature.</li> </ul> <hr/> <p><i>Bug fixes</i></p> <hr/> <ul style="list-style-type: none"> <li>• Added security: Upgraded switch security library. See CVE 2017-3735</li> <li>• Added security: Added extended login time-out feature for consecutive invalid username / password combinations. See CVE-2018-13990</li> <li>• Added security: Removed common key. All keys are now unique. See CVE 2018-13991.</li> <li>• Added security: Web UI pop-up reminder to enable HTTPS or disable the Web agent when saving a non-HTTPS enabled configuration. See CVE 2018-13992</li> <li>• Added security: Implemented Cross-Site Request Forgery protection. See CVE 2018-13993</li> <li>• Added security: Increased web agent denial-of-service immunity. See CVE-2018-13994</li> </ul>
<p>1.34 / 2.00</p> <p>(May / 01 / 2018)</p>	<p><i>Added Functions</i></p> <hr/> <ul style="list-style-type: none"> <li>• Supports Modbus/TCP protocol for switch diagnostics</li> <li>• Added security: Configuration files uploads available in secure binary format</li> <li>• Supports new PoE managed switches (2.xx only)</li> </ul> <hr/> <p><i>Bug fixes</i></p> <hr/> <ul style="list-style-type: none"> <li>• Added security: Prevent stack buffer overflows in index.cgi and menu_pxc.cgi. See CVE-2018-10728</li> <li>• Added security: Prevent stack buffer overflow in shared object file. See CVE-2018-10731</li> <li>• Added security: Prevent direct object reference to read-only switch configuration file. See CVE-2018-10729</li> <li>• Added security: Prevent authenticated remote code execution. See CVE-2018-10730</li> </ul>

Firmware-Version	Functions
1.33  (Dec. / 04 / 2017)	<i>Added Functions</i>
	<ul style="list-style-type: none"> <li>• Added security: HTTPS WebUI connection with TLS only</li> <li>• Added security: HTTPS WebUI connection with TLS 1.2</li> <li>• Added security: Enable/Disable: Monitor Mode, which corrects the issue addressed in CVE-2017-16741</li> <li>• Added security: Force Complex Admin/User Passwords</li> <li>• Added security: Web Server updated to latest version</li> <li>• Added security: Add Secure Web Headers</li> </ul>
	<i>Bug fixes</i>
<ul style="list-style-type: none"> <li>• Added security: Allow HTTPS WebUI connection with updated TLS 1.2 – 2048-bit group (Needs 6 seconds initial web page load time)</li> <li>• Prevent access to any items via Web UI without requiring user or admin account access. See CVE-2017-16743</li> </ul>	
1.32  (Sept. / 02 / 2016)	<i>Added functions</i>
	<ul style="list-style-type: none"> <li>• New Model Support</li> <li>• Added security: SSL updated to latest version</li> </ul>
1.31  (Sept. / 02 / 2016)	<i>Added functions</i>
	<ul style="list-style-type: none"> <li>• Added security: HTTPS WebUI connection with TLS – 2048 bit group (Needs 6 seconds initial web page load time)</li> <li>• Extended Ring fail overs are logged in the “Event Table”</li> <li>• SNTP Server functionality</li> </ul>
	<i>Bug fixes</i>
<ul style="list-style-type: none"> <li>• Added Security: FREAK (Factoring RSA Export Keys) security update in the OpenSSL Library. Removed older ciphers in SSL and TLS</li> <li>• Maintained access for FL View to automatically detect the FL SWITCH 3k / 4k / 48xxE series</li> <li>• Improved display format of the System Date and System Time</li> <li>• SNTP time synchronization can be triggered via SNMP and all IP addressing as well as Poll interval settings are operational</li> <li>• Native VID “saving” issue fixed, when the port member is Untagged</li> <li>• Port Based VLAN can be displayed correctly using the Chrome Web Browser</li> <li>• “Extended Ring” – “Path Control” menu, the “Ring-coupling port of Primary path” setting is consistently maintained after rebooting the switch.</li> </ul>	

Firmware-Version	Functions
1.30  (Aug. / 31 / 2015)	<p><i>Added functions</i></p> <ul style="list-style-type: none"> <li>Extended Ring – Path Control</li> </ul> <p><i>Bug fixes</i></p> <ul style="list-style-type: none"> <li>Improved VLAN setting (Native VLAN ID of the port can be modified, when port is "Tagged")</li> <li>When the QoS is globally enabled, the default "Trust mode" is 802.1p is also enabled</li> <li>Corrected potential for rebooting based on specific topology and specific configuration (LLDP – IGMP – GVRP - STP)</li> <li>SFP combo port (4800E's series) detects its neighbor via LLDP.</li> <li>Global Flow Control web setting also enables flow control on all ports (previously, both Global and individual ports web settings had to be made)</li> </ul>
1.22  (Dec. / 08 / 2014)	<p><i>Added functions</i></p> <ul style="list-style-type: none"> <li>NMS (Network Management Software) added LLDP support</li> <li>Flow control default setting is disabled globally</li> <li>Enhanced SysObjectID table with Phoenix Contact Ethernet devices</li> <li>Added Security: (OpenSSL / Heartbleed and TLS / POODLE Bites security update)</li> </ul> <p><i>Bug fixes</i></p> <ul style="list-style-type: none"> <li>Selectable SSL support (security update)</li> <li>Native VLAN configuration (PVID) independent from the port setting in the Static VLAN Configuration</li> <li>Improved MIB file to the FL SNMP-OPC Server v 3.20</li> <li>Packets with inappropriate VLAN ID are rejected by the Switch IC</li> <li>Basic Serial Interface doesn't display unknown message when configure STP setting</li> <li>Admin path cost of STP Port Config Table page can be set to 0.</li> <li>flWorkNetPortModus of Giga port can display correct value when Giga port set to 1000/FD via web page.</li> </ul>
1.21  (Jan. / 24 / 2014)	<p><i>Added functions</i></p> <ul style="list-style-type: none"> <li>First FW release compatible with FL SWITCH 4800 switches (4800's require separate FW file)</li> </ul> <p><i>Bug fixes</i></p> <ul style="list-style-type: none"> <li>Ring operates when booting up in a linear topology</li> <li>Config file transfer is possible via SNMP</li> <li>Consistent indication of manual port setting via web UI</li> <li>Local Diagnostics page includes complete descriptions</li> <li>All Trap OID's are synchronized to the trap events</li> <li>Extended Ring failure sends a trap message</li> <li>Cold start time decreased</li> </ul>

Firmware-Version	Functions
1.20  (Sept. / 13 / 2013)	<p><i>Added functions</i></p> <ul style="list-style-type: none"> <li>• Dual Ring feature</li> <li>• Selectable direction to Port Mirroring feature</li> <li>• LLDP support in the private MIB</li> </ul> <p><i>Bug fixes</i></p> <ul style="list-style-type: none"> <li>• Modified the Port Mirroring web page for browser compatibility</li> <li>• BootP packet format is now consistent</li> <li>• After power up, the first event is correctly time stamped in the event table</li> <li>• Standard MIB2 sysName and the Phoenix Contact private MIB flWorkBasicName are now consistent</li> <li>• The default STP path cost of the aggregate port meets the latest IEEE specs</li> <li>• Both power supply status are accurately displayed</li> <li>• Isolate SFP setting change from temporarily impacting other SFP on 4008-2SFP switch</li> <li>• Allow a single port to be set as Ring Coupling via SNMP</li> <li>• NTP synchronization operates with the NTP Server</li> <li>• All Link aggregation configuration settings are operational</li> <li>• Can change back to "private" as a password.</li> </ul>
1.10  (Jan. / 18 / 2013)	<p><i>Added functions</i></p> <ul style="list-style-type: none"> <li>• Added LLDP feature</li> </ul> <p><i>Bug fixes</i></p> <ul style="list-style-type: none"> <li>• Long file names are allowed in the Config File Transfer and the Software Update menus.</li> <li>• MAC based security - Global Discard operates consistently</li> <li>• Increase RSTP topology change counter accuracy</li> <li>• GMRP and GVRP work over a link aggregation trunk.</li> <li>• BootP client operates consistently</li> <li>• RSTP port priority setting matches IEEE requirements</li> </ul>
1.00  (March. / 30 / 2012)	<ul style="list-style-type: none"> <li>• First Firmware release</li> </ul>